Name of the Department: EE-VLSI Design and Technology

Name of the Teaching Staff	Mrs.Lakshmamma k m			
Designation	Assistant Professor			
Department	EE-VLSI Design and Technology			
Date of Joining the Institution	07/07/2025			
Contact Details	E mail:lakshmammakmvlsi@skit.org.in			
Qualification with Class / Grade	UG- B.E ECE(FCD)	PG- M.Tech VLSI and Embedder Systems (FCD)	d -	
Total Experience in Years	Teaching – 0	Industry – 1years		
Skills	Modelsim, Cadence, MATlab			
Papers Published in Journals	National - 00	International -0	1	
Papers Presented in Conferences	National - 00	International -0	International -00	
PhD Guide / Give field & University	-	-		
PhDs / Projects Guided	-	-		
Books Published / IPRs/ Patents	-NIL			
Professional Memberships	-			
Consultancy Activities	-NIL			
Awards	-NIL			
Grants fetched	-NIL			
Interaction with Professional Institutions	-NIL			
Other Details(if Any)	-			